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Attorney's Docket No. 42P16680
Confirmation No.: 9123

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Justin K. Brask, et al.

Application No.: 10/750,061

Filed: December 30, 2003

For: NONPLANAR TRANSISTOR WITH
METAL GATE ELECTRODES

Examiner: Lindsay Jr., Walter
Lee

Art Unit: 2812

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications). It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to MS: Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on March 29, 2005
(Date of Deposit)

Dianne Neathery

(Typed or printed name of person mailing correspondence)

Dianne Neathery

(Signature of person mailing correspondence)

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

37 C.F.R. §1.97(b).

37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:

A statement pursuant to 37 C.F.R. §1.97(e) or

A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).

37 C.F.R. §1.97(d). If so, then enclosed with this Information Disclosure Statement are the following:

(1) A statement pursuant to 37 C.F.R. §1.97(e); and

(2) A check for \$180.00 for the fee under 37 C.F.R. §1.17(p) for submission of the Information Disclosure Statement.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: March 29, 2006



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STATEMENT UNDER 37 C.F.R. §1.97(e)

Sir:

I hereby certify (as indicated by an "X" to the left of the appropriate paragraph):

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That each item of information contained in the accompanying Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement.

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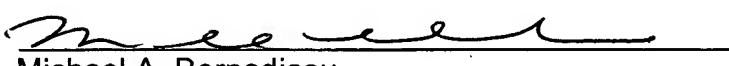
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That no item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned attorney after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to any individual designated in §1.56(c) more than three months prior to the filing the Information Disclosure Statement.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: March 29, 2006


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**O I P E INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(use as many sheets as necessary)

MAR 31 2006
U.S. PATENT & TRADEMARK OFFICE

				Complete if Known	
Application Number				10/750,061	
Filing Date				December 30, 2003	
First Named Inventor:				Justin K. Brask, et al.	
Art Unit				2812	
Examiner Name				Lindsay Jr., Walter Lee	

Sheets & TRADEMARK OFFICE	1	of	3	Attorney Docket No.:	42P16680
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U.S. PATENT DOCUMENTS

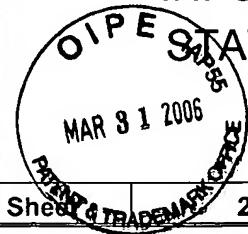
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	US-	2003/0122186 A1	07-03-2003	Sekigawa et al.	
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	US-	2004/0238887 A1	12-02-2004	Nihey	
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Sheet 2 of 3

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Application Number	10/750,061
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		WO 2004/059726 A1	07-15-2004	IBM Corporation		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		International Search Report PCT/US2005/033439, mailed 1/31/2006 (7 pgs.)	
		International Search Report PCT/US2005/035380, mailed 2/13/2006 (14 pgs.)	
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		YANG-KYU CHOI, et al., "A Spacer Patterning Technology for Nanoscale CMOS" IEEE Transactions on Electron Devices, Vol. 49, No. 3, March 2002, pgs. 436-441	
		W. XIONG, et al., "Corner Effect in Multiple-Gate SOI MOSFETs" 2003 IEEE, pgs. 111-113	
Examiner Signature		Date Considered	

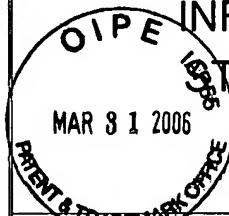
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MAR 31 2006

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	10/750,061
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Sheet	3	of	3
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